

APR 07 2005

PTO/SB/21 (09-04)

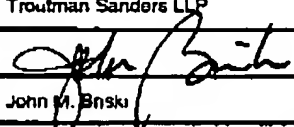
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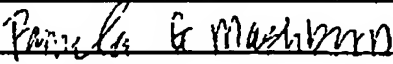
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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10747,824
	Filing Date	December 29, 2003
	First Named Inventor	DIANA, Daniel
	Art Unit	2823
	Examiner Name	Dang, Trung Q
	Attorney Docket Number	INTEL18
Total Number of Pages in This Submission		13

ENCLOSURES (Check all that apply)		
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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In Re Application of:**

**DIANA, Daniel et al.**

**Serial No.: 10/747,824**

**Filed: December 29, 2003**

**Title: METHOD AND APPARATUS FOR FILLING INTERLAYER VIAS ON FERROELECTRIC POLYMER SUBSTRATES**

[illegible]

Group Art Unit: 2823

**Examiner: DANG, Trung Q.**

**Atty. Docket #: INTEL16**

**Assignee: Intel Corporation**

## **AMENDMENT AND RESPONSE TO FIRST OFFICE ACTION**

**Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

**Dear Sir:**

This submission is in response to the Official Action mailed by the U.S. Patent and Trademark Office on February 23, 2005. As of this Official Action, pending claims 1 - 9, and 22 - 25 are allowed, claims 10 - 15 stand rejected, and claims 16, 17, and 19 - 21 are objected to. In response to this Official Action, it is respectfully requested that the Office consider the following remarks and amendments to the above-referenced application.